

REMARKS

Claims 1 to 8, 10 to 18 and 20 to 28 and 30 to 33 are pending this application of which claims 1, 11 and 21 are the independent claims. Favorable reconsideration and further examination are respectfully requested.

Claims 1 to 8, 10 to 18 and 20 to 30 were rejected under 35 U.S.C. § 102 over Rostoker et al. (U.S. Patent No. 5,544,067). As shown above, Applicants have amended the claims to define the invention more clearly. In view of these amendments, withdrawal of the art rejection is respectfully requested.

Claim 1 is directed to a method of generating a logic design for use in designing an integrated circuit (IC). The method includes embedding a computer instruction within a two-dimensional schematic representation of the logic design to produce a unified database representation of the logic design. The computer instruction is devoid of declarations. The two-dimensional schematic representation includes a set of Register Transfer Diagrams (RTD).

The applied art is not understood to disclose or suggest the foregoing features of claim 1. In particular, Rostoker does not disclose or suggest embedding a computer instruction that is devoid of declarations in a two-dimensional schematic representation of a logic design.

As understood by the Applicants, the Examiner indicated during the interview that Rostoker suggests embedding an instruction within a functional block in order to represent a lower-level functional block (see column 27, lines 19 to 42 of Rostoker). Rostoker, however, does not disclose or suggest the content of such an instruction. Accordingly, Rostoker cannot possibly disclose or suggest that the instruction is devoid of declarations.

For at least the foregoing remarks, Applicants believe that claim 1 is allowable.

Claims 11 and 21 roughly correspond to claim 1. Accordingly, claims 11 and 21 are believed to be allowable for at least the same reasons noted above with respect to claim 1.

The dependent claims each incorporate by reference the patentable features noted above. Accordingly, the dependent claims are also believed to be allowable.

It is believed that all of the pending claims have been addressed. However, the absence of a reply to a specific rejection, issue or comment does not signify agreement with or concession of that rejection, issue or comment. In addition, because the arguments made above may not be exhaustive, there may be reasons for patentability of any or all pending claims (or other claims) that have not been expressed. Finally, nothing in this paper should be construed as an intent to concede any issue with regard to any claim, except as specifically stated in this paper, and the amendment of any claim does not necessarily signify concession of unpatentability of the claim prior to its amendment.

In view of the foregoing amendments and remarks, Applicants submit that the entire application is now in condition for allowance. Such action is respectfully requested at the Examiner's earliest convenience.

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Serial No. : 09/942,102
Filed : August 29, 2001
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Attorney's Docket No.: 10559-595001
Intel Docket No.: P12879

No fee is believed to be due for this Response; however, if any fees are due, please apply such fees to Deposit Account No. 06-1050 referencing Attorney Docket 10559-595001.

Respectfully submitted,

Date: 7 July 2004



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